



23/Response
T-Step to
5/13/03

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Serial No. 09/388,857
Filing Date September 1, 1999
Inventor Luan C. Tran
Assignee Micron Technology, Inc.
Group Art Unit 2813
Examiner L.M. Schillinger
Attorney's Docket No. MI22-878
Title: Semiconductor Processing Methods Of Forming Transistors, Semiconductor
Processing Methods Of Forming Dynamic Random Access Memory Circuitry, And
Related Integrated Circuitry

RESPONSE TO JANUARY 3, 2003 OFFICE ACTION

To: Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

From: D. Brent Kenady
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TECHNOLOGY CENTER 2800

Responsive to the Office Action dated January 3, 2003, Applicant amends
and remarks as follows:

AMENDMENTS